

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Fontaine et al.                      Art Unit : 2853  
Serial No. : 10/642,951                      Examiner : Lam S. Nguyen  
Filed : August 18, 2003                      Conf. No. : 4153  
Title : INDIVIDUAL JET VOLTAGE TRIMMING CIRCUITRY

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are submitted. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request. Copies of International Preliminary Report on Patentability in related foreign applications (International Application Nos. PCT/US2005/038743 and PCT/US2004/026964) are also enclosed.

This statement is being filed before the first office action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 8/3/2007

Rex L. Huang  
Rex L. Huang  
Reg. No. 57,661

Fish & Richardson P.C.  
225 Franklin Street  
Boston, MA 02110  
Telephone: (617) 542-5070  
Facsimile: (617) 542-8906

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 09991-042001	Application No. 10/642,951
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Fontaine et al.	
		Filing Date August 18, 2003	Group Art Unit 2853

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,975,672	11/2/1999	Wen	347	015	
	AB	6,046,822	4/4/2000	Wen et al.	358	001.900	
	AC	6,102,513	8/15/2000	Wen	347	015	
	AD	6,217,141	4/17/2001	Nakamura et al.	347	010	
	AE	6,312,076	11/6/2001	Taki et al.	347	010	
	AF	6,352,328	3/5/2002	Wen et al.	347	015	
	AG	6,517,195	2/11/2003	Koeda	347	068	
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
	AL	0 876 915	11/11/1998	EPO			Yes	No
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	David A. Johns et al., "Analog Integrated Circuit Design", John Wiley & Sons, Inc., 1997, pgs. 39-42, 396-397 and 398-400.

Examiner Signature	Date Considered
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EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.